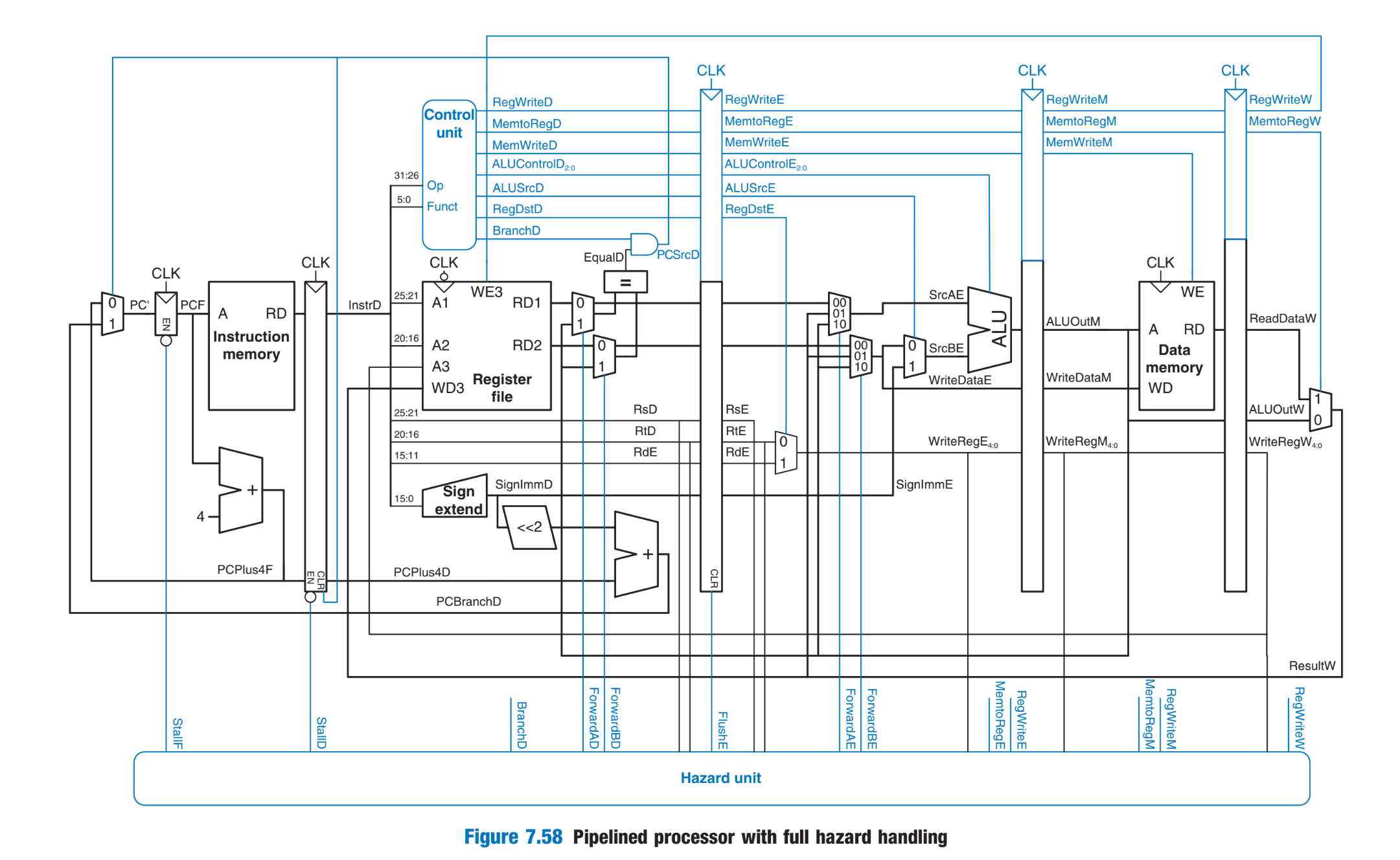
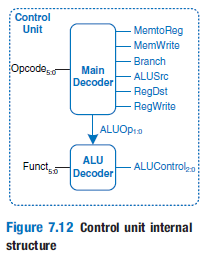
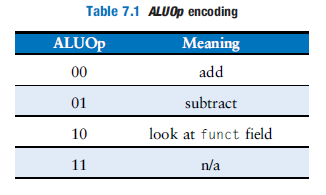
**MIPS pipelined processor**

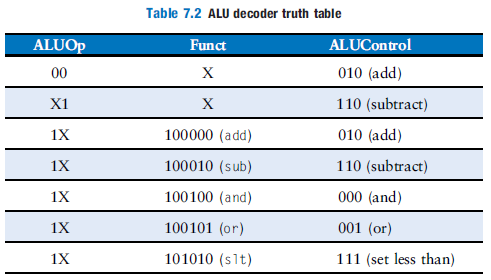


rdD

**ALU OPs:**



**ALU Truth Table:**



**Main Decoder Truth Table:**

