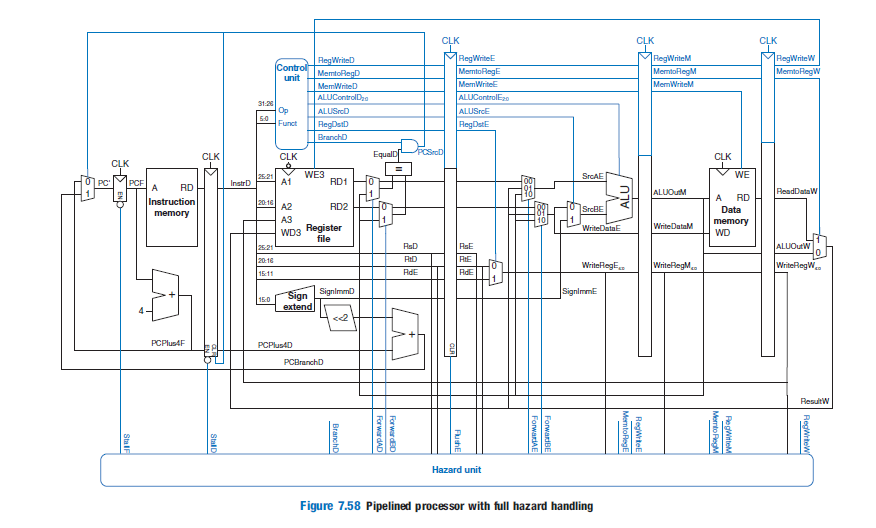
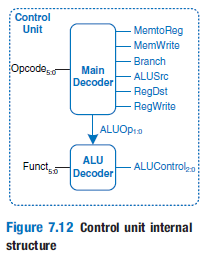
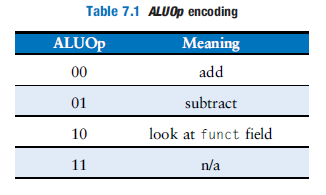
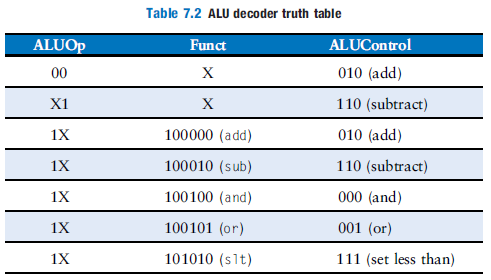
**MIPS pipelined processor**



**ALU OPs:**



**ALU Truth Table:**



**Main Decoder Truth Table:**

